

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((over \$1voltage over adj voltage) and (hous\$3 cas \$3) and strip and slot and (circuit adj board pcb) and (contact with pad with ground) and surge adj arrestors and forked adj contact).clm.	US-PGPUB	OR	ON	2008/12/12 17:51
L2	0	((over \$1voltage over adj voltage) and (hous\$3 cas \$3) and strip and slot and (circuit adj board pcb) and (contact with pad with ground) and surge adj arrestors and forked adj contact).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/12 17:51

L3	0	((over \$1voltage over adj voltage) and (hous\$3 cas \$3) and strip and slot and (circuit adj board pcb) and (contact with pad with ground) and surge adj arrestors and forked adj contact)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/12 17:51
L4	1124	361/119.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/12 17:52

12/ 12/ 2008 6:47:03 PM

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